AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing Of Claims:

1. - 15. (Canceled)

16. (Previously Presented) A method for coordinating network components including at least one first logical component and at least one second logical component, each of the at least one first logical component and the at least one second logical component corresponding to a specific application and communicating with each other via a network on a communication plane, the communication plane being substantially independent from an application plane, the at least one first logical component and the at least one second logical component standing in a master slave relationship to each other, the method comprising the steps of:

in response to a specific event concerning at least one of the at least one first logical component and the at least one second logical component, establishing a communications connection between the at least one first logical component and the at least one second logical component at an initiative of one of the at least one first logical component and the at least one second logical component;

transmitting an information message from the at least one second logical component to the at least one first logical component via the communications connection, the information message including at least information regarding a current application status of the at least one second logical component, the information of the transmitted information message being stored in a first nonvolatile memory of the at least one second logical component, a content of the first nonvolatile memory being actualized every time a change occurs in the application status of the at least one second logical component;

comparing the information of the transmitted information message in the at least one first logical component with information stored in a second nonvolatile memory of the at least one first logical component, the information stored in the second nonvolatile memory concerning at least one of the at least one first logical component and the at least one second logical component; and

coordinating the at least one second logical component on the application plane by the at least one first logical component as a function of a result of the comparison.

17. (Previously Presented) The method according to claim 16, further comprising the step of:

storing the information of the transmitted information message in the second nonvolatile memory of the at least one first logical component.

18. (Previously Presented) The method according to claim 16, further comprising the step of:

after a reset of an already started application, transmitting the information message from one of the at least one first logical component and the at least one second logical component to another of the at least one first logical component and the at least one second logical component.

19. (Currently Amended) The method according to claim 16, wherein [[further comprising the step of]]:

the step of coordinating includes causing the at least one first logical component to reconstruct [[a]] an application state of the at least one first logical component and of the at least one second logical component that was present before the step of establishing by using the information of the transmitted information message and the information stored in the second non-volatile memory of the at least one first logical component.

20. (Previously Presented) The method according to claim 16, wherein:

the step of coordinating includes one of the step of resetting on the application plane and the step of configuring on the application plane.

21. (Previously Presented) The method according to claim 16, wherein:

the method is performed in an automotive on-board information system.

22. (Previously Presented) The method according to claim 16, wherein:

the method is performed for at least one of network-wide failure location and failure diagnosis of the at least one first logical component and the at least one second logical component.